

TITLE: VIDEO DISPLAY GENERATOR

Package : 40 PIN PLASTIC DIL

Operating Temperature Range: 0°C TO + 50°C

Operating Voltage Range : 4.75V TO 5.25V

Pinning requirements:

Inputs

KB1, KB2, KB3, KB4,
MREQ, WR, RD, IORQ,
A14, A15, D5, D7, OSC.

Outputs

INT, RAS, CAS, DRAMWE
ØCPU, ROMCS, U, V, Y,
A1, A2, A3, A4, A5, A6.

INPUT/OUTPUTS

KBO, AO, TAPE/LS,
D0, D1, D2, D3, D4, D6

SUPPLIES

GND = 0V

Vcc1 = 5V±5%

Vcc2 = 2V (NOMINAL)

Issue	1			
Engineer				
Checked				
Date	26.8.82			



D.C. CHARACTERISTICS OVER THE OPERATING TEMP. RANGE. $V_{cc1} = 5V$

PARAMETER	PIN	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{cc1}		$V_{cc1} = 5V$		40		mA
I_{cc2}		$V_{cc2} = 2V$		80		mA
V_{IL}	ALL INPUTS EXCEPT KB0 → KB4 and TAPE/LS				0.8	V
	KB0 → KB4				1.4	V
	TAPE/LS				0.4	V
I_{IL}	ALL INPUTS EXCEPT TAPE/LS	$V_{IN} = 0V$			-0.5	mA
	TAPE/LS	$V_{IN} = 0V$			-1.0	mA
V_{IH}	ALL INPUTS EXCEPT KBO - KB4 AND TAPE/LS		2.0			V
	KB0 → KB4		2.8			V
	TAPE/LS		1.0			V
I_{IH}	KB0 → KB4	$V_{IN} = 5V$			100	μA
	$\overline{MREQ}, \overline{IORQ}, \overline{WR}, \overline{RD}, A14, A15, D5, D7$	$V_{IN} = 5V$			1	mA
	TAPE/LS	$V_{IN} = 3V$			5.0	mA
V_{OL}	$\overline{RAS}, \overline{CAS}, \overline{DRAMWE}, \overline{ROMCS}, A0 \rightarrow A6$	$I_{SINK} = 16mA$			0.5	V
	$\overline{INT}, D0 \rightarrow D4, D6$	$I_{SINK} = 8mA$			0.5	V
	$\overline{\phi CPU}$	$I_{SINK} = 1mA$			0.3	V
V_{OH}	$\overline{RAS}, \overline{CAS}, \overline{DRAMWE}, \overline{ROMCS}, A0 \rightarrow A6$	$I_{OUT} = 400\mu A$	2.4			V
	$\overline{INT}, D0 \rightarrow D4, D6$	$I_{OUT} = 200\mu A$	2.4			V
	$\overline{\phi CPU}$	$I_{OUT} = 1mA$	1.0			V
I_{OFF}		$V_{OUT} = 0V$			-20	μA
	$\overline{RAS}, A0 \rightarrow A6$	$V_{OUT} = 5V$			20	μA